To view this email as a webpage, click here.

Newsletter | October 2025

Editor's Note

Welcome to the October 2025 issue of the Nanotechnology Council newsletter. This issue brings you the latest updates and activities in the IEEE-NTC community. We hope you enjoy it and do let us know if there is any topic you'd like to see covered in the future. All future content submissions to the newsletter should be sent to the editors: Yijun Cui and Ke Chen.

Yijun Cui
Nanjing University of Aeronautics and Astronautics
Nanjing, China

Ke Chen
Nanjing University of Aeronautics and Astronautics
Nanjing, China

Contents

PRESIDENT MESSAGE

Dear NTC Community,

I am excited and proud to announce the launch of our brand-new IEEE Nanotechnology Council (NTC) website: https://ieeenano.org/. This updated platform represents a significant step forward in showcasing who we are as a global community of researchers, engineers, and innovators dedicated to advancing science and engineering at the nanoscale.

The new site is designed to be more dynamic, user-friendly, and engaging. It highlights our conferences, publications, educational and technical activities, and opportunities for involvement — making it easier than ever to explore, connect, and participate in NTC's many activities. It also reflects the energy and vision of our Council as we continue to expand our global reach and impact.

This launch marks an exciting milestone for NTC, but it is also just the beginning. The site will continue to evolve and improve to meet the future needs of our community, ensuring it remains a valuable resource for years to come. I deeply appreciate the many hours of dedication from our NTC leadership team and volunteers — especially Ed Perkins and Georgios Sirakoulis — as well as the creative support from the team at I Heart Blank LLC, who made this launch possible!

We warmly welcome your feedback to help us in this effort. Please feel free to share your thoughts with Ed Perkins (e.perkins@ieee.org) and me (jwkim@uark.edu).

Together, let's continue to shape the future of nanotechnology.

With pride and gratitude,

Jin-Woo Kim
President, IEEE Nanotechnology Council

BREAKING NEWS

Call for Proposals for Future Sites for IEEE-NANO 2028 and IEEE NMDC 2027

The IEEE Nanotechnology Council (NTC) is calling for proposals for future sites for IEEE-NANO 2028 and IEEE NMDC 2027

A. Call for proposals for future site for IEEE-NANO 2028

Initial Proposal Deadline: 31 October 2025

The IEEE International Conference on Nanotechnology (IEEE-NANO) is the flagship IEEE Nanotechnology conference. The conference scope covers a wide range in nanoscience and technology. In particular, it covers AI in Nanotechnology, Emerging Plasma Nanotechnologies, Heterogenous Integration and Chiplets, Modeling and Simulation, Nano-Acoustic Devices, Processes, and Materials, Nano-Biomedicine, Nanoelectronics, Nano-Energy, Environment, and Safety, Nanofabrication, Nanomagnetics, Nanomaterials, Nano-Metrology and Characterization, Nano-Optics, Nanophotonics, and Nano-Optoelectronics, Nanopackaging, Nanorobotics and Nanomanufacturing, Nanoscale Communications, Nanosensors and Nanoactuators, Nanotechnology for Soft Electronics, Quantum, Neuromorphic, and Unconventional Computing, and Spintronics. NANO 2024 was held in Gijon, Spain. Recent conferences were in Jeju, Korea (2023), Balearic Islands, Spain (2022), Macau (2019), Cork (2018), Pittsburgh (2017), Sendai (2016), Rome (2015), and Toronto (2014). NANO 2020 and 2021 were held virtually due to the pandemic. NANO typically runs between early- to mid-July. We are now seeking proposals for IEEE-NANO 2028 which is expected to run in IEEE Region R1-6/7/9 (North and Latin America).

For conference history, visit **https://ieeenano.org/ieee-nano-conferences/**.

B. Call for proposals for future site for IEEE NMDC 2027

Proposal Deadline: 31 October 2025

The IEEE Nanotechnology Materials and Devices Conference (IEEE NMDC) aims to develop critical assessment of existing work and future directions in nanotechnology research from every sector in the nanotechnology research field, with a special focus on materials and devices. NMDC 2023 was held in Paestum, Italy and NMDC 2024 in Salt Lake City (USA). Past locations of the NMDC have been in Asia (Korea, Japan, Taiwan, Singapore, Nanjing), North America (California, Michigan, Hawaii, Alaska, Oregon), Canada (Vancouver, BC) and Europe (Italy, France, Sweden). NMDC typically runs in early- to mid-October.

We are now seeking proposals for IEEE NMDC 2027 which is expected to run in IEEE Region 10 (Asia & Pacific).

For conference history, visit **https://ieeenano.org/nmdc**.

PROPOSAL FORMAT:

Initial proposals should be prepared in powerpoint presentation format. **NTC Conference Proposal PowerPoint template** should be used.

Formal proposals must be e-mailed prior to the appropriate deadline to: NTC

Vice-President for Conferences John Yeow (**jyeow@uwaterloo.ca**) with copies to Ed Perkins, NTC Secretary (**e.perkins@ieee.org**).

Contact John Yeow (**jyeow@uwaterloo.ca**) with any questions.

Early indication of an intention to submit a proposal is strongly recommended. Candidates for NANO 2028 and NMDC 2027 will be expected to present a 5-10 minute "Proposal in Preparation" based on their initial proposal at the Fall NTC Conference Committee meeting, and a complete proposal at the NTC Excom and AdCom meetings in July 2026 held at IEEE-NANO 2026 in Nanjing, China.

Special Topic on Non-traditional Computing for Energy Efficiency

Guest Editors

Kerem Y. Camsari, University of California Santa Barbara, camsari@ucsb.edu

Supriyo Datta, Purdue University, datta@purdue.edu

Editor-in-Chief

Azad Naeemi, Georgia Institute of Technology, azad@gatech.edu

Aim and Scope

Computing now consumes a significant portion of global electricity production, with AI and data center workloads projected to reach larger scales if current trends continue. The continued success of computing technologies depends on dramatic improvements in energy efficiency across

devices, circuits, and systems. While CMOS scaling has powered decades of progress, it now faces fundamental limits in power and density and is increasingly mismatched to the needs of data-intensive applications such as artificial intelligence, optimization, and edge computing. This has motivated a broad search for **non-traditional computing approaches** that harness physical dynamics, stochasticity, memory-in-logic integration, and new materials to move beyond the von Neumann paradigm.

Non-traditional computing spans a wide spectrum: from new devices such as spintronic, ferroelectric, photonic, superconducting, and phase-transition elements, to oscillator and probabilistic networks, to hybrid CMOS + X architectures that co-design physics and algorithms. These approaches promise energy savings through new forms of computation rooted in **natural dynamics**. Realizing this potential requires coordinated advances across the technology stack. Their impact depends on bridging scales from material and device physics, to compact models, to system-level demonstrations and applications in machine learning, optimization, and scientific computing.

This Special Topic of the *IEEE Journal on Exploratory Solid-State Computational Devices and Circuits (JxCDC)* will highlight advances in **energy-efficient non-traditional computing** that show the potential for significant reductions in power consumption across devices, circuits, architectures, and algorithms. We invite contributions showing physical realizations, modeling, simulation, and co-design strategies that push the frontiers of exploratory computing beyond conventional computing paradigms.

Topics of Interest

Topics of interest include but are not limited to:

- **Device primitives**: spintronic, ferroelectric, photonic, superconducting, resistive, correlated-electron, and phase-transition devices for non-traditional computing
- **Stochastic and dynamical elements**: probabilistic bits, noisy switching devices, oscillator and neuromorphic circuits, networks exploiting natural dynamics
- **Circuits and architectures**: neuromorphic processors, memory-in-logic, hybrid CMOS + X systems (e.g., CMOS-spintronic, CMOS-memristive), distributed and parallel architectures such as Ising and oscillator networks
- **Modeling and simulation**: first principles and physics-based models, compact device models, multiscale frameworks linking materials to circuits, benchmarking methodologies
- **Algorithm-hardware co-design**: physics-inspired algorithms leveraging stochasticity and dynamics, hybrid probabilistic-deterministic implementations, and frameworks for large-scale optimization and generative AI
- **Quantum-inspired approaches**: tensor networks, variational algorithms implemented classically, quantum annealing inspired architectures
- **Experimental validations**: prototype demonstrations, proof-of-concept systems, comparative studies with CMOS baselines
- **Applications**: energy-efficient hardware for generative AI, machine learning, combinatorial optimization, scientific computing
- **Cross-cutting issues**: variability and noise as computational resources, reliability and scalability, heterogeneous integration with CMOS, and energy-efficiency metrics across devices and systems

Submission Guidelines
Submit your paper through the JXCDC submission site.

Deadlines

Open for Submission: 15 October 2025
Submission Deadline: 15 January 2026
First Notification: 15 February 2026
Revision Submission: 1 March 2026
Final Decision: 15 March 2026
Online Special Topic Publication: 1 April 2026

Papers submitted earlier than the submission deadline will be reviewed upon submission and will get published earlier than the timeline listed above.

CHAPTERS

IEEE Shanghai NTC Chapter Successfully Hosts Special Lecture on Cutting-Edge Energy Materials

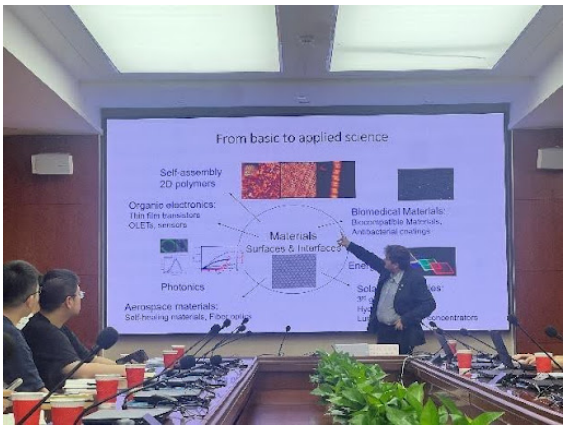
Shanghai, 22 September 2025 – At the invitation of the IEEE Shanghai Nanotechnology Council (NTC) Chapter, **Prof. Federico Rosei** from the University of Trieste, Italy, delivered an academic lecture titled "Energy Challenges from a Materials Perspective" at the Shanghai Institute of Technical Physics, Chinese Academy of Sciences. The event was chaired by **Weida Hu, Chair of the IEEE Shanghai NTC Chapter**, and attended by Academician Xuechu Shen of the Chinese Academy of Sciences. **Vice-Chair Fang Wang**, along with more than 50 Chapter members, researchers, and students, actively participated, creating a vibrant academic atmosphere.

Prof. Rosei, an internationally renowned scholar in materials science and energy technology, is an **IEEE Senior Member** and a recipient of several IEEE honors including the IEEE Montreal Gold Medal and the IEEE Canada Outstanding Engineer Award. He also serves as a Distinguished Lecturer for multiple IEEE Societies. In his presentation, he systematically elaborated on the latest advances in third-generation photovoltaics, solar hydrogen production, luminescent solar concentrators, and other cutting-edge technologies from the perspective of structure-property relationships in materials, emphasizing the critical role of key raw materials in next-generation energy devices.

This event not only represented a high-level academic exchange for the State Key Laboratory of Infrared Physics but also highlighted IEEE's significant role in promoting global collaboration in optoelectronic technology and energy materials. By inviting a world-leading scholar with strong IEEE affiliations, the IEEE Shanghai NTC Chapter effectively enhanced its influence within the academic community across East China and nationwide, strengthened international academic ties, and provided local members with a valuable opportunity for face-to-face interaction with an international authority.

Following the lecture, attendees engaged in a lively discussion with Prof. Rosei on topics such as nanomaterial preparation, device integration, and industrialization prospects. Academician Shen spoke highly of the presentation, noting that such high-level international exchanges are of great importance for the growth of young Chinese researchers.

The successful execution of this activity underscored IEEE's core values in promoting technological innovation, knowledge dissemination, and talent development. It has also laid a solid foundation for the Chapter to organize more high-quality international academic events in the future.



TECHNICAL ACTIVITY

2025 IEEE NTC TC10 Modeling and Simulation October Webinar

Date: 30 October 2025, 17:00
Time: 17:00 Berlin/Central European Time (16:00 UK, 12:00 New York, 09:00 California)
Speaker: Prof. Vladimir Fomin, Leibniz Institute for Solid State and Materials Research Dresden
Title: An Introduction to the Physics of Quantum Rings

Registration link

Abstract

An adequate modeling of the self-organized quantum rings is possible only on the basis of the modern characterization of those nanostructures. We discuss an atomic-scale analysis of the indium distribution of self-organized InGaAs quantum rings (QRs). The analysis of the shape, size and composition of self-organized InGaAs QRs at the atomic scale reveals that AFM only shows the material coming out of the QDs during the QR formation. The remaining QD material, as observed by Cross-Sectional Scanning Tunneling Microscopy (X-STM), shows an asymmetric indium-rich crater-like shape with a depression rather than an opening at the center and determines the observed ring-like electronic properties of QR structures. A theoretical model of the geometry and materials properties of the self-organized QRs are developed on that basis and the magnetization is calculated as a function of the applied magnetic field. Although the real QR shape differs strongly from an idealized circular-symmetric open-ring structure, Aharonov-Bohm-type oscillations in the magnetization have been predicted to survive. They have been observed using the torsion magnetometry on InGaAs QRs. Large magnetic moments of QRs are shown to originate from dissipationless circulating currents in the ground state of an electron or hole in the QR.

Bio

Prof. Fomin is internationally recognized as an expert in nanophysics, covering a broad spectrum of topics such as the theory of 3D nanoarchitectures, topology- and geometry-driven effects in quantum rings, strain-induced self-rolled and direct-write micro- and nanoarchitectures, phase boundaries and vortex matter in micro- and nanoarchitectures and patterned superconductors. He has also made significant contributions to understanding the superconducting properties of metallic nanograins, phonons, vibrational excitations, polarons, electronic transport and optical effects in multilayer structures and superlattices, magnetopolarons and magneto-Raman scattering in monolayer Transition Metal Dichalcogenides. Additionally, his work extends to exploring topological states of light and spin-orbit coupling in microcavities, optical properties of quantum dots, thermoelectric properties of semiconductor nanostructures, quantum transport in sub-0.1 μm semiconductor devices, propulsion mechanisms of catalytic tubular micromotors, and theory of self-propelled micromotors for cleaning polluted water. His service as the Scientific Editor of the Encyclopedia of Condensed Matter Physics, 2nd Edition (Elsevier, 2024) and editor of three editions of Physics of Quantum Rings (Springer Nature, 2013, 2018 and 2025) further demonstrates his expertise and authority in the field. Prof. Fomin established a fundamental paradigm for superconductor 3D nanoarchitectures: a complex geometry leads to a strong inhomogeneity of the normal-to-the-surface magnetic field and to a non-trivial topology of screening supercurrents underlying novel physics of vortices and phase slips

screening supercurrents underlying novel physics of vortices and phase slips.

YOUNG PROFESSIONALS

Call for NTC YP Ambassadors

The IEEE Nanotechnology Council (NTC) Young Professionals (YPs) and the Vice-President for Educational Activities, supported by the ExCom, have established a new program called NTC YPs AMBASSADORS. This program has the objective of inspiring and informing NTC YPs on a variety of topics (e.g., technical, nontechnical) to enhance their interest and engagement in the field of nanoscience and nanotechnology. The NTC YPs ambassadors will deliver talks at various Chapters/Sections "on-demand" and in a virtual modality.

Eligibility & Applications:

The call for nominations is starting from **1 November** with a deadline for **21 December 2025**.

The candidate must:

-be an IEEE member
-NTC participant
-belong to the IEEE YP
-have about 5 years or more experience reflecting professional maturity (e.g, university, professional, volunteering, etc.)

The ambassadors are selected for a term of one year. The appointment starts from **1 January 2026**. All new ambassadors ***are required to give at least two talks during their tenure of one year***.

For the application the following details are needed:

i) a resume or CV (two pg. max.);
ii) the title(s) of the maximum 2 talks;
iii) a brief writeup on the benefits of the talk proposed to the NTC YPs community;
iv) a 5-min short video sample of any proposed talk.

The applications must be sent to matteobrunolodi@ieee.org

Evaluation: a dedicated and specific Subcommittee will manage the nomination and selection process of the NTC YPs AMBASSADORS and provide the results that will be announced both on the NTC Website (https://ieeenano.org/) and on the TryNano webpage (https://trynano.org/it/)."

IEEE Nanotechnology Council Young Professionals Webinar

Explore the frontiers of atomic-scale computing and discover how Field-coupled Nanocomputing (FCN) is reshaping logic design beyond CMOS technologies.

Title: **Atomic Scale Computing: Chip Design for Field-coupled Nanotechnologies**

Speakers: Mr. Simon Hofmann & Dr. Marcel Walter

Date: **Tuesday 25 November**
Time: **14:00 (GMT+1)**

Free and open to all

Register here

About the Talk:

Atomic Scale Computing is a key frontier for beyond-CMOS innovation, with Silicon Dangling Bond (SiDB) devices pushing miniaturization to its limits. This webinar introduces the Munich Nanotech Toolkit (MNT)-an open-source framework addressing the design automation challenges of Field-coupled Nanocomputing.

The discussion will also feature the Graph-Oriented Layout Design (GOLD) algorithm and the MNT Bench and MNT Designer tools-powerful innovations enabling scalable and efficient FCN circuit design.

Meet the Speakers:

Dr. Marcel Walter - Postdoctoral Researcher at TUM and Senior Quantum, Software Engineer at Munich Quantum Software Company. Initiator of the fiction framework and aigverse library for FCN and design logic synthesis.

Mr. Simon Hofmann - Ph.D. candidate at TUM and Quantum Software Engineer at Munich Quantum Software Company, focusing on design automation for FCN technologies.

Join us and explore how atomic-scale innovation and design automation are driving the next generation of computation technologies.

IEEE Nanotechnology Council @ NAP 2025 AI Panel & Young Professionals Networking Event

The IEEE Nanotechnology Council invited students and Young Professionals to join at the Nanomaterials: Applications & Properties (NAP) 2025 conference in Braitslava, Slovakia, for a unique event.

Monday 8 September 2025
4:00 - 6:30 PM

The program consisted of an inspiring panel on "**AI in Research and Nanotechnology**" featuring Prof. Tymofiy Mylovanov (President, Kyiv School of Economics), exploring how AI is reshaping research practices, opportunities, and ethical challenges in nanotechnology. Following the discussion and Q&A, participants were invited to an **IEEE Young Professionals Social & Networking Event**. This informal gathering will connect students, early-career researchers, and industry leaders—creating the perfect space to exchange ideas, build collaborations, and expand professional networks.

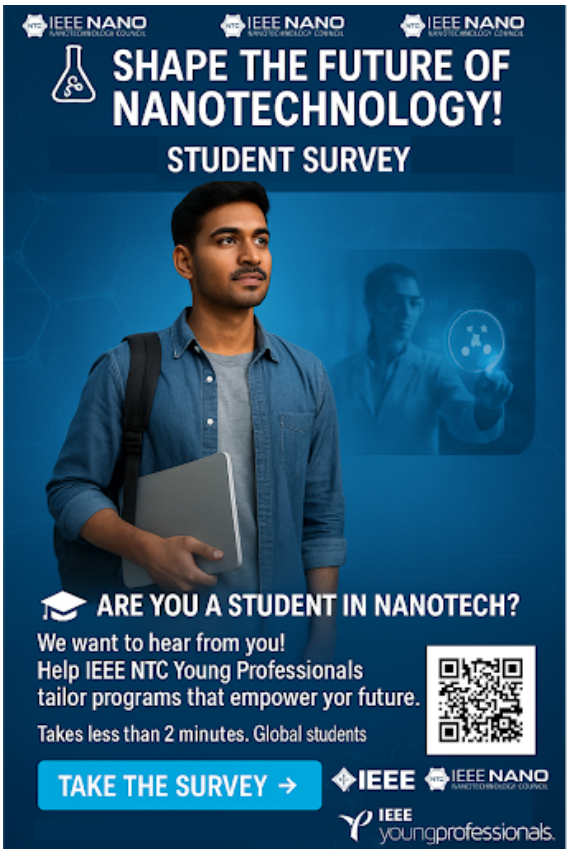
Interested in joining?

- Gain fresh insights on AI in nanotechnology from a unique, cross-disciplinary perspective
- Meet peers, experts, and industry representatives in a welcoming environment
- Expand your professional network at the start of the conference

- Expand your professional network at the start of the conference

Stay updated via the [IEEE NTC YPs LinkedIn pages].

NTC Young Professionals: Call for Survey Participation for Students & YPs



Are You a Student or Young Professional Interested in Nanotechnology?

Take ONLY 2 minutes to shape the future of the IEEE Nanotechnology Council Young Professionals!

We're building initiatives that truly serve the next generation of innovators in nanoscience, nanoengineering, and nanotechnology. But we need your voice to guide us!

Why Participate?

Because your input will help shape student support programs, networking events, grants, and career development resources that you actually need.

Just a few quick questions – no long forms, no fluff.

Take the survey now and make your voice count!

https://forms.gle/keAjdUR5az4PDvrWA

Open to undergraduate, master's, PhD students and Post Doc worldwide.

NSF Graduate Student Fellowship and Career Development

Panel at IEEE NANO 2025

The 25th IEEE International Conference on Nanotechnology (IEEE NANO 2025) was held 13-16 July 2025, in Washington, DC, USA.

IEEE NANO 2025 offered graduate student fellowships, sponsored by the U.S. National Science Foundation (NSF), to support graduate students based in the United States. The fellowships were established to encourage student participation by enabling recipients to attend the conference and present their research.

Eligibility for the NSF Graduate Student Fellowship required applicants to be full-time graduate students (PhD or Master's) at a U.S. university and the presenting author of a full paper or abstract at the conference. Application packages included a CV, the submitted paper or abstract, and a letter of support from the applicant's supervisor. The selection committee consisted of Prof. Michael Adachi, Prof. Xiaoning Jiang, and Prof. Yong Zhu. Fellowship applications were evaluated based on merit, financial need, and contribution to the diversity of conference participation. Following a rigorous review process, 18 graduate students—including 6 female students—were awarded the fellowship. The award covered conference registration and expenses for a two-night hotel stay.

In addition to the Graduate Student Fellowship, NSF support also sponsored a Career Development Panel held on Tuesday 15 July from 12:30 to 1:30 pm. The panel addressed challenges faced by young professionals in nanotechnology, including graduate students, postdoctoral researchers, and early-career faculty. The international panel featured Prof. Michael Dickey (USA), Prof. Kremena Makasheva (France), Prof. Rafal Sliz (Finland), and host Prof. Yong Zhu (USA). Panelists shared their personal experiences in graduate school and their transitions to independent careers, followed by an engaging Q&A session with the audience.

We gratefully acknowledge the financial support of the NSF. Special thanks go to Professors Yong Zhu, Xiaoning Jiang, Orlin Velev, Amay Bandodkar, and Jong Eun Ryu for leading the successful NSF proposal. We also extend our appreciation to the IEEE NANO 2025 organizers and the IEEE Nanotechnology Council leadership for their valuable support.

NTC Young Professionals Update

Five Regional NTC YP LinkedIn pages have been established and are timely updated. The regional coordinators established cooperation with the regional NTC conference organizers in order to support and ensure presence of young professionals.

NTC YP LinkedIn:

- Region 7 (Canada)
- Region 9 (Latin America)
- NTC YP India
- Region 8 (Africa, Europe, Middle East)
- Region 10 (Asia and Pacific)

CONFERENCES

Included in this section:

- **IEEE NANO 2026 – Call for Papers**
- **IEEE LANANO**
- **IEEE-NANOMED 2025**
- **Call for Proposals for Future Site for IEEE-NANO 2028**

IEEE NANO 2026 – Call for Papers

Since founded in 2001, IEEE-NANO is the flagship conference series of the IEEE Nanotechnology Council (NTC). It promotes advanced research in nanoscience and nanotechnology. Recent conferences were held in Washington DC (USA, 2025), Gijón (Spain, 2024), Jeju Island (Korea, 2023), Palma de Mallorca (Spain, 2022), Montreal (Canada, 2021), Virtual (2020), Macao (China, 2019), Cork (Island, 2018), Pittsburgh (USA, 2017), Sendai (Japan, 2016), Rome (Italy, 2015), and Toronto (Canada, 2014).

The 26th IEEE International Conference on Nanotechnology (IEEE-NANO 2026) will be held from July 5 – 8, 2026, in Nanjing, China. We are looking forward to your visit in Nanjing, China.

Website URL: https://2026.ieeenano.org/
Date: 5-8 July 2026

Important Dates:

Special Session Proposal Submission: 15 Jan 2026
Special Session Proposal Decisions: 1 Feb 2026
Full Paper Submission: 1 Mar 2026
Full Paper Decisions: 10 Apr 2026
Abstract Only Submission: 10 Apr 2026
Abstract Only Decisions: 1 May 2026

IEEE-LANANO 2025 – Call for Registration

Inspired by the success of previous events organized by the IEEE NTC-Peru Chapter (IEEE Nano Peru 2021, 2022, 2023, and 2024), we are proud to announce the inaugural IEEE Latin American Conference on Nanotechnology (IEEE-LANANO 2025). This international conference is co-sponsored by the IEEE Nanotechnology Council and is designed to foster global collaboration and accelerate the exchange of knowledge in nanoscience and nanotechnology throughout Latin America.

IEEE-LANANO 2025 will serve as a vibrant platform for scientists, engineers, and educators from around the world to showcase cutting-edge research, share insights, and drive interdisciplinary innovation in the field.

Upcoming Dates:

Registration deadline: 24 October 2025

IEEE-NANOMED 2025

Website URL: http://ieee-nanomed.org/2025/
Date: 1 – 4 December
Venue: Hong Kong, China

IEEE-NANOMED is one of the premier annual events organized by the IEEE Nanotechnology Council (NTC), and brings together physicians, scientists, and engineers from all over the world and every sector of academy and industry for the advancement of basic and clinical research in medical and biological sciences through nano/molecular medicine and engineering. Attendees of IEEE-NANOMED can share their latest research in engineering and nano/molecular medicine with other practitioners in their field and related fields, ranging from basic scientific and engineering research to translational and clinical research.

Important Dates:

Two-Page Abstract Deadline: 11 July 2025
Notification of Acceptance: 12 September 2025
Full Paper Deadline: 12 July 2025 (for best paper competition)
3 October 2025 (for inclusion in IEEE *Xplore*)

Early Bird Registration: 30 September 2025

PUBLICATIONS

IEEE Transactions on Nanotechnology

View the full current issue of IEEE T-NANO.

For additional information, visit the IEEE *Xplore* website.

To find how to submit to T-NANO, click here.

T-NANO, VOLUME 24

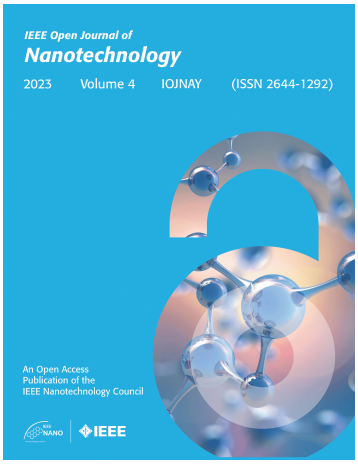
IEEE Open Journal of Nanotechnology

View the full current issue of IEEE OJ-NANO.

For additional information, visit the IEEE *Xplore* website.

To find how to submit to OJ-NANO, click here.

OJ-NANO, VOLUME 6



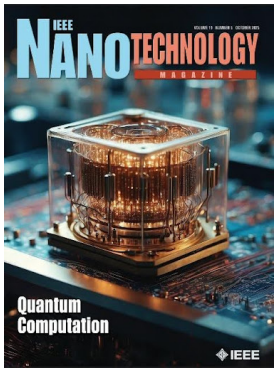
IEEE Nanotechnology Magazine

View the full current issue of IEEE INM.

For additional information, visit the IEEE *Xplore* website.

For information on submitting to INM, click here.

INM, VOLUME 19, NO. 4



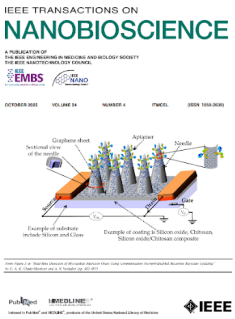
IEEE Transactions on NanoBioscience

View the full current issue of IEEE T-NB.

For additional information, visit the IEEE *Xplore* website.

To find how to submit to T-NB, click here.

T-NB, VOLUME 24, NO. 4



LIST OF NANOTECHNOLOGY COUNCIL OFFICERS (2025)

President

Jin-Woo Kim

Past-President

Fabrizio Lombardi

President-Elect

Kremena Makasheva

Vice President for Conferences

John Yeow

Vice-President for Educational Activities

Luca Pierantoni

Vice-President-elect for Educational Activities

Georgios Sirakoulis

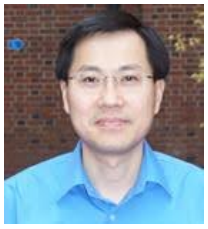
Vice President for Finances

Malgorzata Chrzanowska-Jeske

Vice-President for Publications

Georgios Sirakoulis

Vice President-elect for Publications



Xiaoning Jiang

Vice President for Technical Activities

Weiqiang Liu

Vice President-elect for Technical Activities

Jie Han

Secretary

Edward G. Perkins

If you have an IEEE Account, manage your IEEE communications preferences here. Users without an IEEE Account can access the Privacy Portal to view selected preferences and policies.

Unsubscribe | Website | Privacy Policy | Contact

© {{my.copyright year}} IEEE– All rights reserved.

IEEE • 445 Hoes Lane, Piscataway, NJ 08854 USA